

Notice of References Cited	Application/Control No. 10/754,550	Applicant(s)/Patent Under Reexamination CHO, JANG-HO	
	Examiner Benjamin P. Geib	Art Unit 2181	Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	A	US-6,073,159	06-2000	Emer et al.	718/103
*	B	US-6,658,447	12-2003	Cota-Robles, Erik	718/103
*	C	US-6,076,157	06-2000	Borkenhagen et al.	712/228
	D	US-			
	E	US-			
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	"Exploiting Choice: Instruction Fetch and Issue on an Implementable Simultaneous Multithreading Processor"; Tullsen et al; 1996
	V	"Balancing Throughput and Fairness in SMT Processors"; Luo et al; 2001; IEEE
	W	"Front-End Policies for Improved Issue Efficiency in SMT Processors"; Ali El-Moursy and David Albonesi; 2002; IEEE
	X	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.